

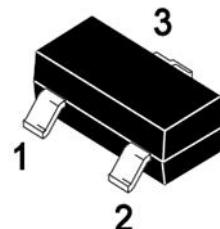
SSCP114EGS7

PNP Type Digital Transistor (built-in resistors)

➤ Features

VCC	VIN	IO	R2/R1 Typ.
-50V	-40~+10V	-50mA	1

➤ Pin configuration

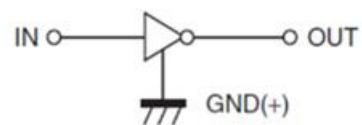
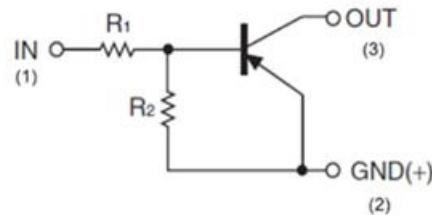


SOT-323

➤ Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects. Only the on/off conditions need to be set for operation, making the device design easy.



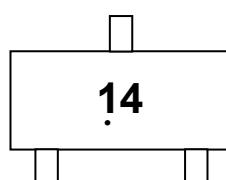
Circuit Diagram

➤ Applications

- Amplifying signal
- Electronic switch
- Oscillating circuit
- Variable resistance

➤ Ordering Information

Device	Package	Shipping
SSCP114EGS7	SOT-323	3000/Reel



Marking(Top View)

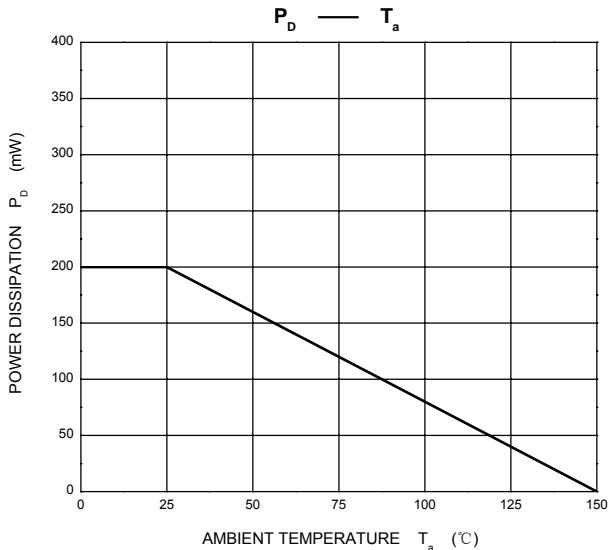
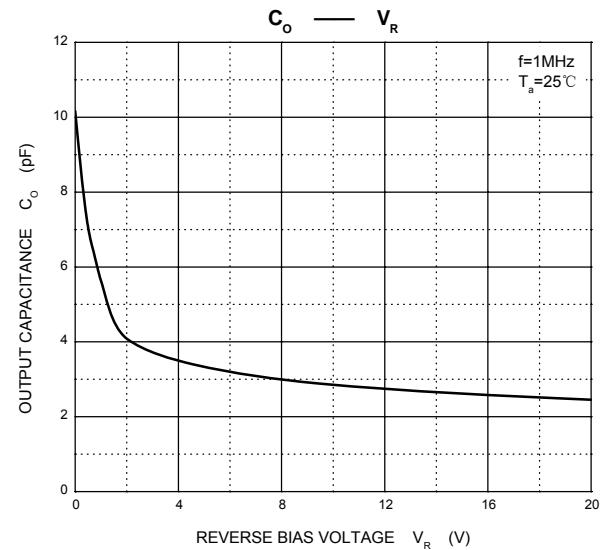
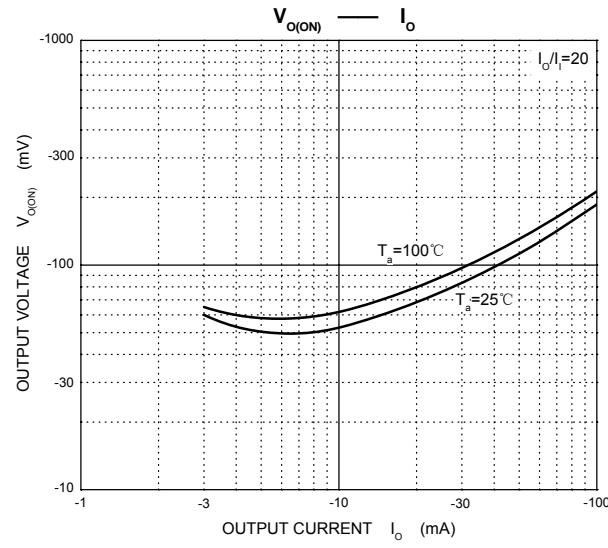
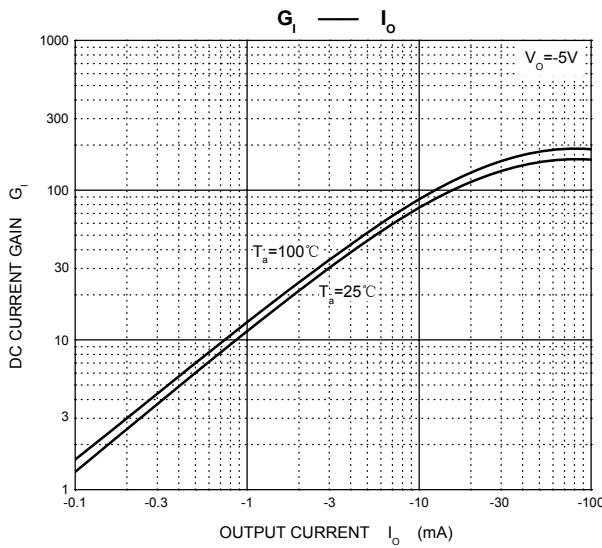
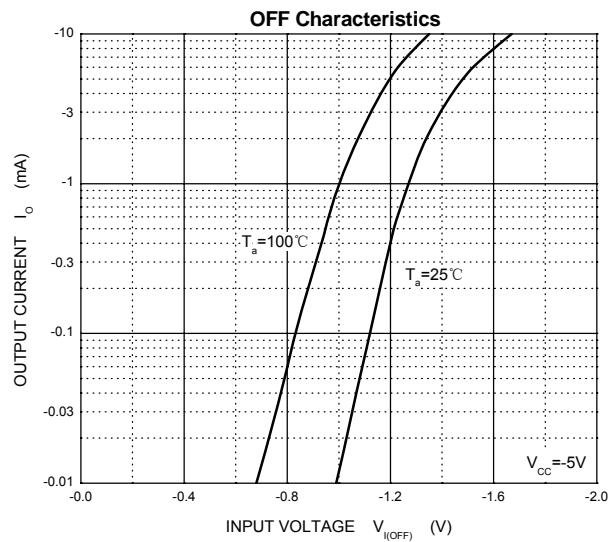
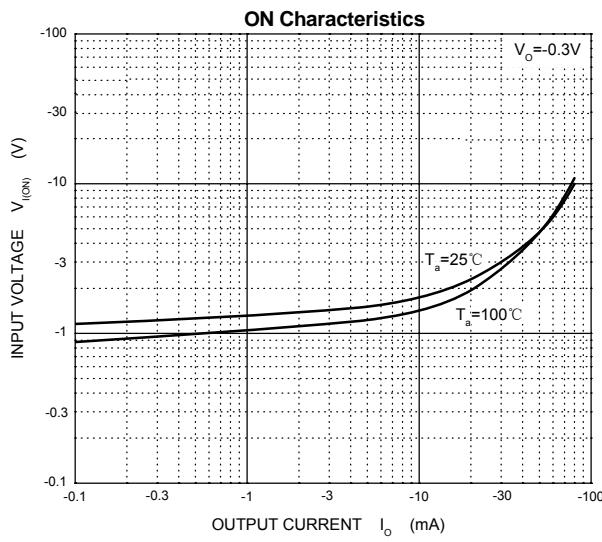
➤ Absolute Maximum Ratings($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	-50	V
Input Voltage	V_{IN}	-40 to +10	V
Output current	I_O	-50	mA
Collector Power Dissipation	P_C	200	mW
Junction Temperature	T_J	-55 to 150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^\circ\text{C}$

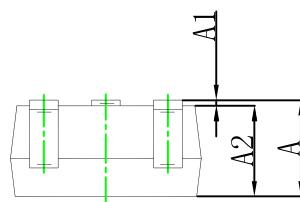
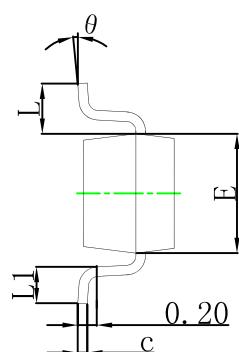
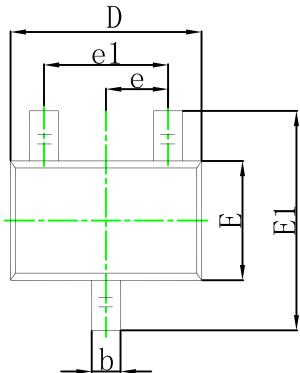
➤ Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input Voltage	$V_{I(off)}$	$V_{CC} = -5\text{V}$, $I_O = -100\mu\text{A}$	-0.5			V
	$V_{I(on)}$	$V_{CC} = -0.3\text{V}$, $I_O = -10\text{mA}$			-3	V
Output Voltage	$V_{O(on)}$	$I_O/I_I = -5\text{mA} / -0.5\text{mA}$			-0.3	V
Input Current	I_I	$V_I = -5\text{V}$			-0.88	mA
Output Current	$I_O(off)$	$V_{CC} = -50\text{V}$, $V_I = 0\text{V}$			-0.5	uA
DC Current Gain	G_1	$V_O = -5\text{V}$, $I_O = -5\text{mA}$	30			
Input Resistance	R_I		7	10	13	$\text{K}\Omega$
Resistance Ration	R_2/R_1		0.8	1	1.2	
Transition Frequency	f_T	$V_{CE} = -10\text{V}$, $I_O = -5\text{mA}$, $f = 100\text{MHz}$		250		MHz

➤ **Typical Performance Characteristics ($T_a=25^\circ\text{C}$ unless otherwise noted)**

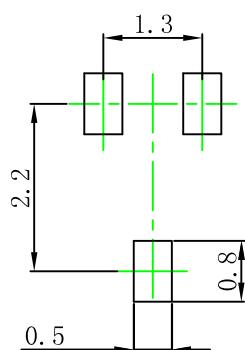


- Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

SOT-323 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.

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